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CONFIRMATION NO. 4292

SERIAL NUMBER 10/664,541	FILING DATE 09/19/2003  RULE	CLASS 711	GROUP ART UNIT 2185	ATTORNEY DOCKET NO. TI-27700A
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## APPLICANTS

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} OK-SE

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/410,772 10/01/1999 PAT 6,629,223 } OK-SE

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

EUROPEAN PATENT OFFICE (EPO) 99400472.9 02/26/1999

EUROPEAN PATENT OFFICE (EPO) 98402455.4 10/06/1998

} OK-SE

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 12/19/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY FRANCE	SHEETS DRAWING 10	TOTAL CLAIMS 43	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials <i>SE</i>				

## ADDRESS

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## TITLE

Method and apparatus for accessing a memory core multiple times in a single clock cycle

OK-SE

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